

## Annex A

### Artist's impression of CETRAN Test Circuit



The CETRAN Test Circuit that will be jointly developed by LTA and JTC to support the Centre's research on testing standards for SDVs in Singapore.

Photo credit: JTC



Industry players will be able to tap on the future CETRAN Test Circuit to test and develop their own SDVs, encouraging industry partnerships and the development of future mobility solutions.

Photo credit: JTC